

**REMARKS**

Reconsideration of the application in light of the following remarks is respectfully requested.

Claims 1-22 are pending.

Claims 1, 9, 11, 18-19 and 22 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over U.S. Patent No. 5,649,083 to Barkans et al. ("Barkans") in view of U.S. Patent No. 5,469,190 to Masterson. Because Barkans and Masterson fail to disclose or suggest a one-dimensional conversion table, Applicants respectfully traverse the rejection.

The Examiner contends that Barkans discloses the features recited in claims 1, 9, 11, 18-19 and 22, however, the Examiner acknowledges that Barkans does not disclose a one-dimensional conversion table. The Examiner contends that Masterson discloses a display device with a one-dimensional conversion circuit, and states that it would have been obvious for a person of ordinary skill in the art at the time of the invention to have combined Barkans and Masterson to achieve the invention of claims 1, 9, 11, 18-19 and 22.

Independent claims 1, 9, 11 and 18 recite a "a one-dimensional bit conversion table." Applicants submit that Masterson fails to disclose a one-dimensional bit conversion table. In contrast, Masterson discloses a conversion circuit 16 that converts data from a twenty-four bits per pixel format to a fifteen bits per pixel format, or from the fifteen bit format back to the twenty-four bit format. Masterson discloses that "the conversion is simply accomplished by concatenating three additional zeroes to the least significant end of the five bits of data stored for

each component of each pixel.” (Masterson, column 6, lines 10-17, and Figs. 4 and 6.) A conversion circuit, which merely concatenates “three additional zeroes to the least significant end” of data, is not a one-dimensional bit conversion table.

Because Masterson does not disclose “a one-dimensional bit conversion table,” as recited in claim 1, the combination of Barkans and Masterson neither discloses nor suggests the invention of independent claims 1, 9, 11 and 18. Therefore, the Examiner has failed to meet the burden of establishing a *prima facie* case of obviousness against claims 1, 9, 11 and 18. Claim 19 depends from claim 1. Claim 22 depends from claim 11. Applicants submit that claims 19 and 22 are patentable over Barkans and Masterson for at least the same reasons as their respective base claims. Withdrawal and reconsideration of the rejection is requested.

Claims 2, 10, 20 and 21 stand rejected under 35 U.S.C. § 103(a) as being unpatentable over Barkans in view of Masterson and U.S. Patent No. 5,394,518 to Friedman et al. (“Friedman”). Applicants respectfully traverse the rejection.

Independent claims 2 and 10 recite a “one-dimensional bit conversion table.” The Examiner cites Friedman as disclosing “a display device comprising G component (6 bits) > R component (5 bits) > B component (4 bits).” (Detailed Action, page 3, item 3.) The Examiner contends that Barkans, Masterson, and Friedman discloses or suggests all the features of independent claims 2 and 10.

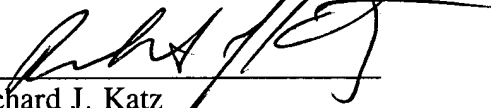




If there are any other issues remaining which the Examiner believes could be resolved through a Supplemental Response or an Examiner's Amendment, the Examiner is respectfully requested to contact the undersigned at the telephone number indicated below.

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Respectfully submitted,

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